Amendments to the Claims

Please amend claims 1, 2, 10 & 11 as set forth below, and cancel claims 3-6, 8, 9, 12-15, 17, 18, 21 & 22 without prejudice. In accordance with current amendment practice, all pending claims are reproduced below. Changes in the amended claims are shown by underlining (for added matter) and strikethrough or double brackets (for deleted matter).

1. (Currently Amended) A method for controlling power of a computer in which at least a power-on self test for hardware is carried out before shifting into an operating system process, when the power is turned on, comprising:

obtaining a result of said power-on self test; [[and]]

automatically turning on the power again a defined period of time after stopping the power supply to said computer when a predetermined test result of said power-on self test has been obtained[[.]];

wherein operation of a hardware component contained in said computer is stabilized when said predetermined test result has been obtained; and

wherein stabilizing the operation of said hardware component comprises employing at least one of a setup for specifying a pulse width modulation type using a switching signal at a fixed frequency for the power supply and a setup for disabling a function to suppress power consumption.

2. (Currently Amended) The method according to Claim 1, wherein said predetermined test result includes a test result indicating that [[a]] the hardware component contained in said computer is not initialized correctly.

3. Canceled.

4. Canceled.

5. Canceled.

6. Canceled.

7. (Previously Presented) The method according to Claim 1, wherein the power is inhibited to be turned on again after the power supply to said computer is stopped when said predetermined test result has been obtained predetermined number of times.

8. Canceled.

9. Canceled.

10. (Currently Amended) A power control apparatus for controlling power of a computer in which at least a power-on self test for hardware is carried out before shifting into an operating system process, when the power is turned on, comprising:

      a holding unit for obtaining a result of said power-on self test;
[[and]]

      a control unit for automatically controlling turn on of power again a defined period of time after stopping the power supply to said computer when a predetermined test result of said power-on self test has been obtained by said holding unit[[.]];

      wherein operation of a hardware component contained in said computer is stabilized when said predetermined test result has been obtained; and

      wherein stabilizing the operation of said hardware component comprises employing at least one of a setup for specifying a pulse width modulation type using a switching signal at a fixed frequency for the power supply and a setup for disabling a function to suppress power consumption.

11. (Currently Amended) The power control apparatus according to Claim 10, wherein said predetermined test result includes a test result indicating that [[a]] the hardware component contained in said computer is not initialized correctly.

12. Canceled.

13. Canceled.

14. Canceled.

15. Canceled.

16. (Previously Presented) The power control apparatus according to Claim 10, wherein said control unit inhibits the power to be turned on again after the power supply to said computer is stopped when said predetermined test result has been obtained a predetermined number of times.

17. Canceled.

18. Canceled.

19. (Original) A computer, comprising:

a power control apparatus for controlling power of said computer according to Claim 10;

a power unit being controlled by said power control apparatus; and

a computer load operating on the power supplied by said power unit.

20. (Original) A computer, comprising:

a power control apparatus for controlling power of said computer according to Claim 16;

a power unit being controlled by said power control apparatus; and

a computer load operating on the power supplied by said power unit.

21. Canceled.

22. Canceled.

23. (Previously Presented) The method according to claim 1, wherein said automatically turning on occurs without making an operator of the computer aware of said result of said power-on self test.

24. (Previously Presented) The power control apparatus according to claim 10, wherein the automatically controlling turn on of power occurs without making an operator of the computer aware of said result of said power-on self test.

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